

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Hiroyuki Miyake et al. Art Unit : Unknown
Serial No. : New Application Examiner : Unknown
Filed : April 2, 2004
Title : SOURCE FOLLOWER, VOLTAGE FOLLOWER, AND SEMICONDUCTOR
DEVICE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Applicants submit the references listed on the attached form PTO-1449.

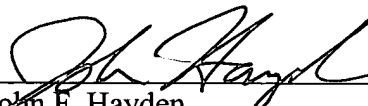
Submitted herewith is an English translation of the following foreign language
references, or portions thereof:

Desig. ID	Source
AL	Japan 11-073165: English Abstract – DIALOG (R) File 345: Inpadoc/Fam. & Legal Stat (2004); Accession No. 14947921 and family to U.S. Patent No. 6,313,819 (Desig. ID “AC”)
AM	Japan 2000-194327: English Abstract – DIALOG (R) File 345: Inpadoc/Fam. & Legal Stat (2004); Accession No. 16185257
AN	Japan 2000-347159: English Abstract – DIALOG (R) File 347: JAPIO (2003); Accession No. 06761288
AO	Japan 2001-083924: Full English Language Translation

This statement is being filed with the application. Accordingly, only copies of foreign
patent documents and non-patent literature are enclosed. Please apply any charges or credits to
Deposit Account No. 06-1050.

Respectfully submitted,

Date: April 2, 2004



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Substitute Form PTO-1449 (Modified) Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b))	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 12732-226001	Application No. New Application
	Applicant Hiroyuki Miyake et al.		
	Filing Date April 2, 2004	Group Art Unit	

U.S. Patent Documents							
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA	5,689,205	11/18/1997	Hughes et al.			01/11/1996
	AB	5,783,952	07/21/1998	Kazazian			09/16/1996
	AC	6,313,819	11/06/2001	Maekawa et al.			08/28/1998
	AD	6,445,371	09/03/2002	Miyazawa et al.			06/06/2000
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

Foreign Patent Documents or Published Foreign Patent Applications								
Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
	AL	11-073165	03/16/1999	JAPAN			ABS	
	AM	2000-194327	07/14/2000	JAPAN			ABS	
	AN	2000-347159	12/15/2000	JAPAN			ABS	
	AO	2001-083924	03/30/2001	JAPAN			FULL	
	AP							

Other Documents (include Author, Title, Date, and Place of Publication)		
Examiner Initial	Desig. ID	Document
	AQ	Y. Kida et al.; "LN-4: A 3.8 inch Half-VGA Transflective Color TFT-LCD with Completely Integrated 6-bit RGB Parallel Interface Drivers"; <i>EURODISPLAY 2002</i> ; pp. 831-834; 2002
	AR	Joon-Chul Goh et al.; "A New Poly-Si TFT Pixel Circuit Scheme for an Active-Matrix Organic Light Emitting Diode Displays"; <i>Asia Display / IDW '01</i> ; pp. 319-322; 2001
	AS	H. Sekine et al.; "Amplifier Compensation Method for a Poly-Si TFT LCLV with an Integrated Data-Driver"; <i>1997 SID</i> ; pp. 45-48; 1997
	AT	

Examiner Signature	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	